

REPLACEMENT SHEET

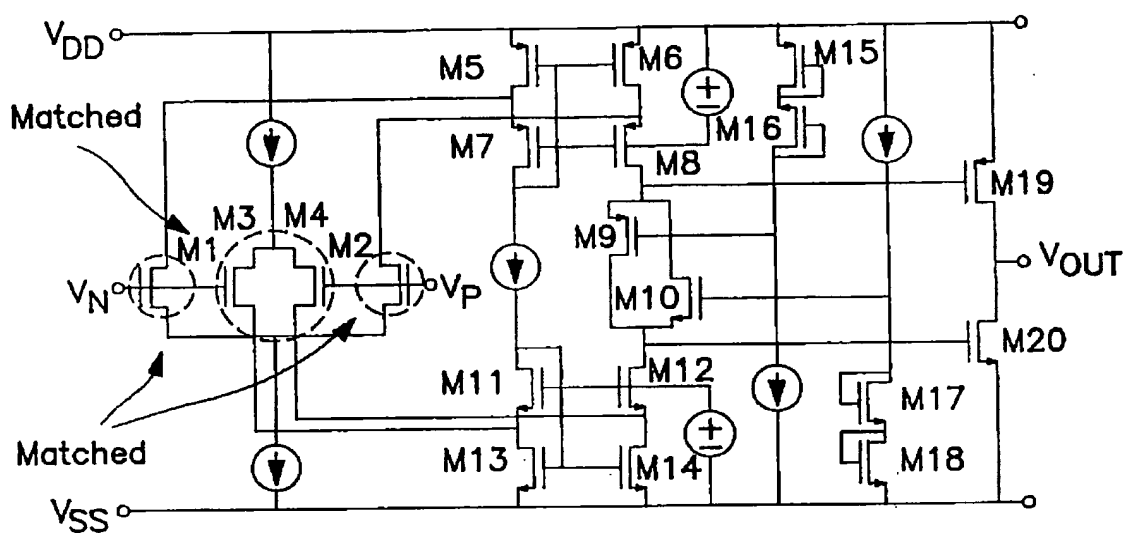


FIG. 1

REPLACEMENT SHEET

Impact of mismatch on offset voltage of an opamp

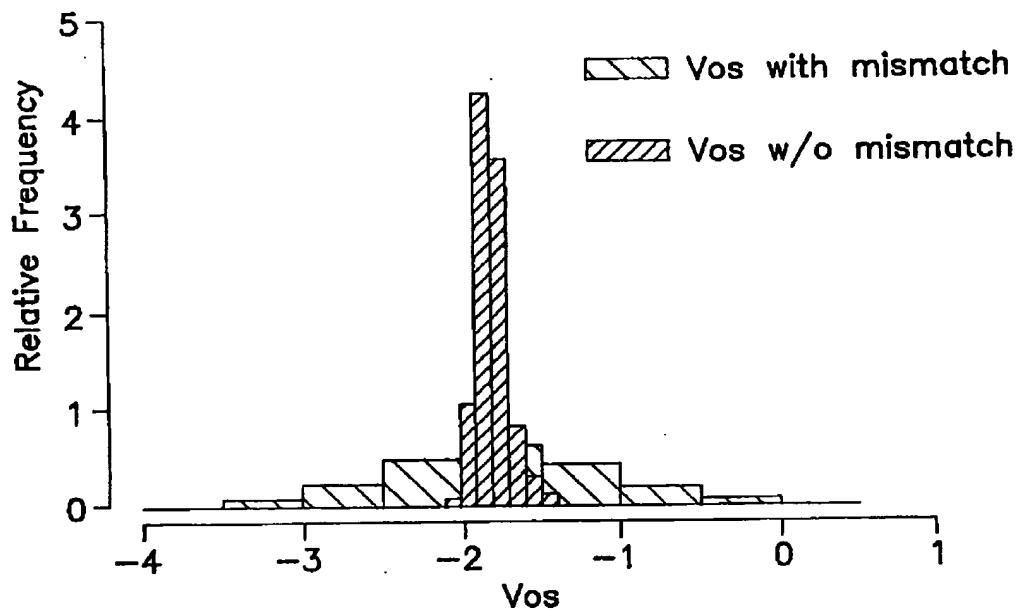


FIG. 2

Std dev of DNL for a 8 bit DAC

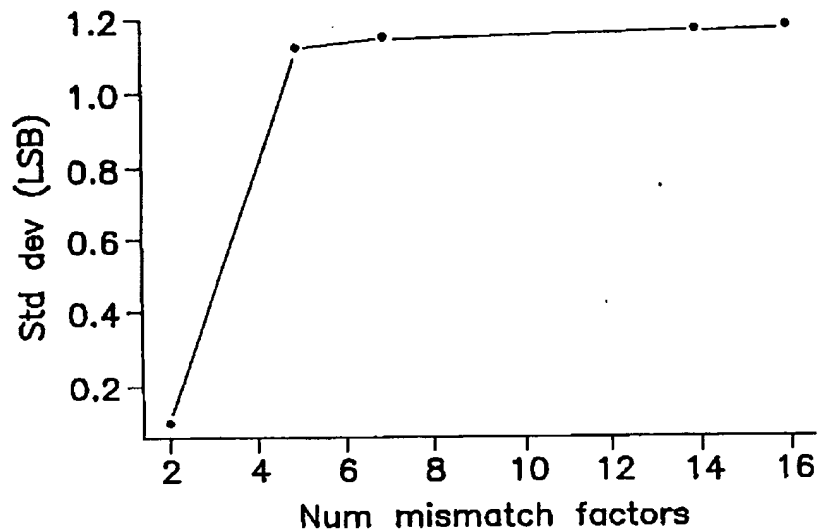


FIG. 4

REPLACEMENT SHEET

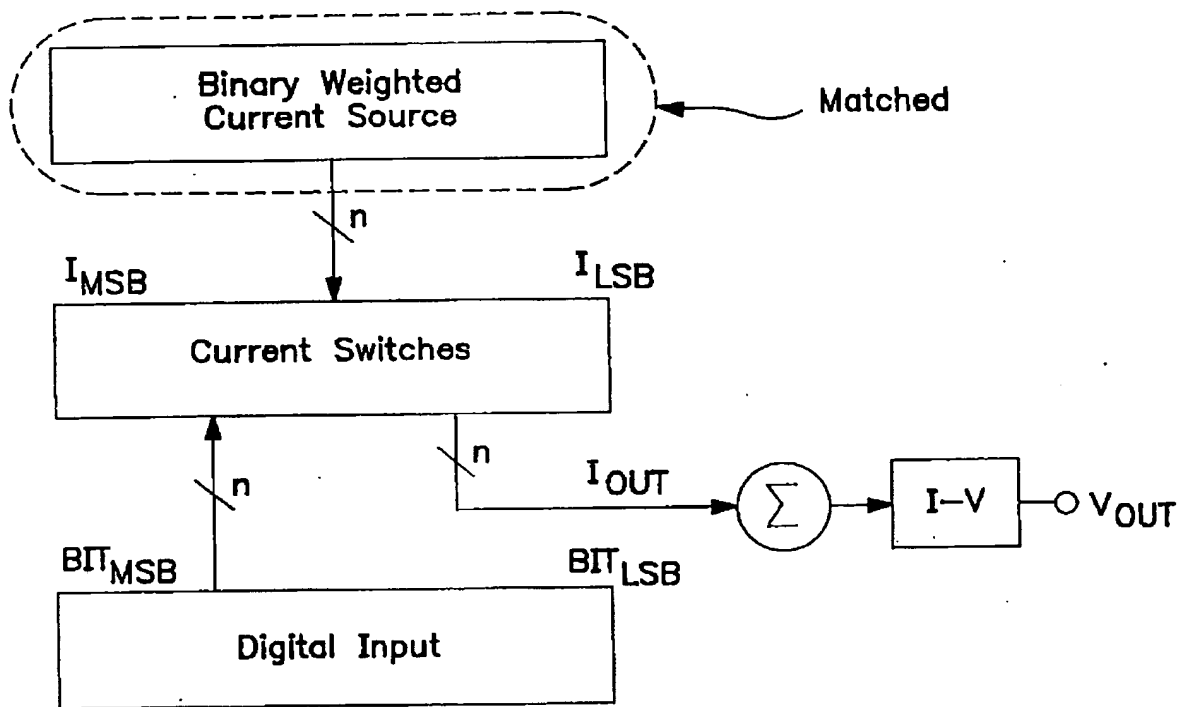


FIG. 3